## 04000MR404052002

Reg No.:

Name:

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Eighth Semester B.Tech Degree Supplementary Examination August 2021

## **Course Code: MR404**

## Course Name: POWER ELECTRONICS AND DRIVES

Max.	M	arks: 100 Duration: 3	Hours
1	5	PART A	
		Answer all questions, each carries 5 marks.	Marks
k		Draw and explain the V-I characteristics TRIAC.	(5)
2		Compare power MOSFET with BJT	(5)
3		List various application of phase controlled rectifier.	(5)
4		Explain type A and type B DC chopper.	(5)
5		List some of the advantages and applications of chopper system.	(5)
6		Explain half wave inverter with waveform.	(5)
7		What is a cycloconverter? Enumerate some of its applications.	(5)
8		Briefly explain different parts of an electric drives.	(5)
		PART B	
		Answer any three full questions, each carries 10 marks.	
9 8	a)	Explain modes of operation of SCR using the V-I characteristics.	(5)
- A	b)	Briefly explain the thyristor protection techniques.	(5)
10		Discuss the operation of power MOSFET and explain transfer, output and	(10)
		switching characteristics of power MOSFET.	
11		Discuss the operation of single phase full bridge controlled converter with R	(10)
		load. Also draw the output wave form and find the equation for average output	
		voltage.	
12		With necessary circuit diagram and waveform explain the working of current	(10)
		commutated chopper.	
13		With neat sketch and waveforms explain the working of three phase $180^{\circ}$ mode	(10)
		operation of inverter.	
		PART C	
		Answer any two full questions, each carries 15 marks.	
14 a	a)	Explain single phase voltage controller with resistive load.	(8)

b) Describe multistage sequence control of voltage controller. (7)

## 04000MR404052002

15		Describe the operation of three phase to three phase cycloconverter.	(15)
16	a)	Describe different types of load torques.	(8)
	b)	Briefly explain factors affecting for the choice of electric drives.	(7)
17	a)	Explain briefly about the steady state stability.	(7)
21. 2	b)	Explain about the four quadrant operation of drives with neat diagram.	(8)