

C 48874

Name .....

Reg. No. ....



**FOURTH SEMESTER B.TECH. (ENGINEERING) DEGREE EXAMINATION**  
**JUNE 2008**

CS 04 405—COMPUTER ORGANIZATION AND DESIGN  
(2004 admissions)

Maximum : 100 Marks

Time : Three Hours

**Part A**

*Answer all questions.*

- I. (a) Write short notes on evaluation of performance.  
(b) Write short notes on computer registers and instructions.  
(c) Write the booth algorithm.  
(d) Write addition and subtraction algorithm.  
(e) Write short notes on Arithmetic logic shift unit.  
(f) Write about Register transfer.  
(g) Write about memory hierarchy.  
(h) Write about Cache memory.

(8 × 5 = 40 marks)

**Part B**

- II. (a) Explain in detail about operations and operands of computer hardware.  
*Or*  
(b) Write about instruction codes and Timing and control of computer.
- III. (a) Explain the working of float point adder with a neat diagram and flowchart.  
*Or*  
(b) Explain in detail about ALU.
- IV. (a) Write about Arithmetic logic shift unit and address sequence of microprogram.  
*Or*  
(b) Explain how microprogrammed control unit is designed.
- V. (a) Explain in detail about modes of transfer and asynchronous data transfer.  
*Or*  
(b) Explain in detail about types and characteristics of input/output devices.

(4 × 15 = 60 marks)