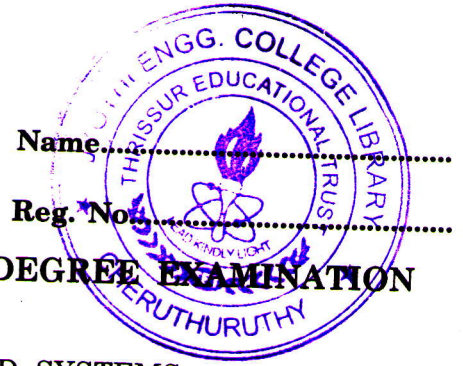


C 58253



Name .....

Reg. No. ....

**FOURTH SEMESTER B.TECH. (ENGINEERING) DEGREE EXAMINATION  
JUNE 2009**

**CS 2K 404—ELECTRONIC CIRCUITS AND SYSTEMS**

Time : Three Hours .

Maximum : 100 Marks

*Answer all questions.*

- I. (a) Write the operation of clamping circuit.  
(b) Write the concept of bistable multivibrator.  
(c) Write the concept of VLSI.  
(d) Construct CMOS-NOR gate.  
(e) Write the basic concepts of ROM.  
(f) Write the drawbacks of weighted resistor D/A converter.  
(g) What is meant by 'internal noise' ?  
(h) Draw the basic block diagram of communication system.
- II. (a) Discuss the principle of monostable multivibrator with neat circuit. (8 × 5 = 40 marks)  
*Or*  
(b) Explain the principle of astable multivibrator with a neat circuit diagram.
- III. (a) Explain the ECL operation with a neat circuit diagram. (15 marks)  
*Or*  
(b) Write the concepts of totempole TTL and open collector TTL. (15 marks)
- IV. (a) Draw the block diagram of dual slope A/D converter and explain. (15 marks)  
*Or*  
(b) Discuss the concept of magnetic surface storage devices. (15 marks)
- V. (a) Explain the principle of amplitude modulation and demodulation with necessary equations. (15 marks)  
*Or*  
(b) Discuss the role of radiation and propagation of electromagnetic waves in communication system. (15 marks)
- [4 × 15 = 60 marks]