C 58196

Name..

Reg. No...

FOURTH SEMESTER B.TECH. (ENGINEERING) DEGREE EXAMINATION JUNE 2009

CS 04 406—ELECTRONIC CIRCUITS AND SYSTEMS

(2004 admissions)

Time : Three Hours

Maximum : 100 Marks

Part A

Answer **all** *questions.*

1. What is a clipper ? Explain briefly.

2. Need for bootstrap sweep generator.

3. Distinguish different logic levels.

4. Explain the concept of SSI and MSI and differentiate them.

5. What is programmable ROM ? Explain briefly.

6. What is memory expansion ?

7. Distinguish External and Internal noise.

8. Briefly explain FM demodulation.

$(8 \times 5 = 40 \text{ marks})$

Part B

1. Draw the clamping circuits and explain in detail with help of waveforms.

Or

Explain Bistable multivibrator with a help of a neat sketch.

(15 marks)

2. Explain the concepts of SSI, MSI, LSI and VLSI. Differentiate them and explain in detail.

Or

Explain TTL flip-flops in detail with neat sketch.

(15 marks)

3. Explain about the different classifications made in ROM.

Or

Explain DAC and ADC in detail with neat sketch.

(15 marks)

4. Explain frequency and phase modulation in detail.

Or

Explain in detail about the radiation and propagation of EM waves.

(15 marks)

$[4 \times 15 = 60 \text{ marks}]$