R3964

Reg No.:

Name:

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSIT THIRD SEMESTER B.TECH DEGREE EXAMINATION DECE

Course Code: CS207

Course Name: ELECTRONIC DEVICES AND CIRCUITS

Max. Marks: 100

PART A

Duration: 3 Hours

ges: 2

Answer all questions, each carries3 marks. Marks 1 Draw the circuit of a RC differentiator and write its design equation. Also (3)draw its output waveform for a 1KHz square wave input. 2 Compare the features of BJT with FET. (3)3 What is line regulation and load regulation? Explain with equation for (3)percentage of regulation 4 Draw a sweep circuit with transistor acting as a switch. (3)

PART B

Answer any two full questions, each carries9 marks.



What is the function of above circuit?,

What will happen:

8

(i) if polarity of diode is changed.

(ii) if a reference voltage of (-3 volts) is connected in series with the diode.

(iii) Plot output wave forms in all cases.

b) Explain working of a voltage Tripler with the help of relevant circuit (3)

- 6 a) Give the necessity of Current fold back and current limit protection. (9) Explain with circuit and graphs.
- 7 a) Draw the structure of depletion mode MOSFET and explain its operation (7) with characteristics.
 - b) Distinguish between enhancement and depletion mode MOSFETs

PART C

Answer all questions, each carries 3 marks.

Give the importance of biasing in transistors? Mention significance of (3)

Ε

(6)

(2)

operating point.

Ε

9	What is the effect of cascading in gain and bandwidth of	(3)
	Amplifier?	
10	What are the conditions for getting sustained oscillations?	(3)
11		(2)

11 Mention the difference between positive and negative feedback? Give one (3) application of each.

PART D

Answer any two full questions, each carries 9 marks.

12		Voltage divider biasing is most widely used one in amplifiers. Why?	(9)
		Explain with the help of required equations and circuit.	
13	a)	Design a Hartley oscillator to generate a frequency of 150KHz.	(5)
	b)	Draw the circuit of RC coupled amplifier and explain the function of each component.	(4)
14		With circuit diagram and design equations explain the working of a monostable multivibrator	(9)
		PART E	
		Answer any four full questions, each carries 10 marks.	
15	a)	What are the features of ideal op-amp?	(2)
	b)	With circuits and equations show that an op-amp can act as integrator,	(8)
		differentiator, adder, subtractor.	
16	a)	Define: (1) Slew rate, (2) CMRR, (3) offset voltage (4) Offset current	(4*2=8)
	b)	What are their practical values for parameters of op-amp IC 741	(2)
17		With circuit, relevant equations and waveforms explain the working of a Schmit trigger	(10)
18	a)	Describe the working of a binary weighted D/A Converter, with example	(3)
	b)	Draw the circuit and frequency response of active lowpass and high pass filters .Also draw the circuit of a second order active low pass filter	(7)
19	a)	Explain the working of any one type of ADC.	(8)
	b)	What are their important specifications?	(2)
20	a)	With functional block diagram, explain the working of 555 Timer IC.	(4)
	b)	Write design equations and pin out of 555 TIMER IC working as astable	(6)
		multivibratot to generate a wave form of 1KHz., with 50% duty cycle.	
	2	****	