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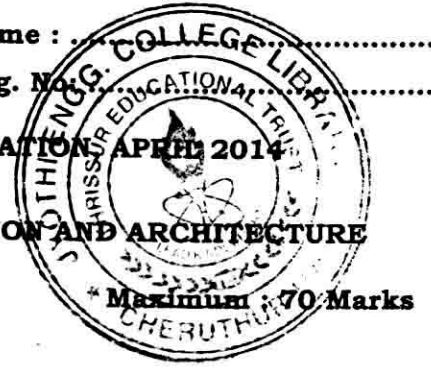
Name :

Reg. No.

FOURTH SEMESTER B.TECH DEGREE EXAMINATION APRIL 2014
(09 Scheme)

EC 09 405 / PTEC 09 404 - COMPUTER ORGANIZATION AND ARCHITECTURE

Time : Three Hours



PART A

1. What is an Accumulator register?
2. What is address mapping?
3. What is a Bus?
4. Give any two register transfer instructions.
5. Give the syntax for a subroutine subprogramme.

(5 x 2 = 10 Marks)

PART B

Answer any four questions

6. With suitable examples explain fixed point and floating point representations.
7. What are Multilevel Memories? Explain.
8. Write a note on Multiprocessor.
9. Explain DMA.
10. Explain I/O mapped I/O.
11. Explain the Memory Mapped I/O.

(4 x 5 = 20 Marks)

PART C

12. Discuss in detail about the types of Instructions with suitable examples.
(Or)
13. Explain Hardwired control and Micro programmed control with suitable examples.
14. Explain Memory technology and compare memory structure and performance.
(Or)
15. Describe Cache memory and its significance.
16. Explain bus control and system control.
(Or)
17. Discuss in detail about I/O processor and its operation with the command word.
18. Explain 8085 microprocessor architecture.
(Or)
19. Discuss the various interrupts supported by 8085.

(4 X 10 = 40 Marks)
