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Name

FIFTH SEMESTER B.TECH. (ENGINEERING) DEGRE EXAMINATION, DECEMBER 2011

IT 04 502-DIGITAL DATA COMMUNICATION

(2004 admissions)

Maximum: 100 Marks Time : Three Hours Answer all questions.

Part A

I. (a) List out the advantages of distributed processing.

- (b) Contrast an analog with a Digital signal.
- (c) Explain bit synchronization.
- (d) Explain how checksum is used for error detection.
- (e) Discuss on link utilization.
- (f) Explain the mechanism of Go back N for error control.
- (g) What is byte stuffing in BSC?
- (h) Explain briefly about half duplex protocols.

 $(8 \times 5 = 40 \text{ marks})$

Part R (8 marks) II. (a) (i) Explain DTE-DCE interface.

(ii) Explain the categories of guided media in brief. (7 marks)

(8 marks)

(b) (i) Discuss the functions of each layer of OSI model. (ii) Explain in detail about frequency division multiplexing. (7 marks) Or

III. (a) Describe the mechanism of cyclic redundancy check with an example.

(b) Write short notes on :

(8 marks) (i) Dynamic Huffman coding.

(ii) Transmission control circuits. (7 marks)

IV. (a) Write in brief about the following error control strategies:

(i) Stop and wait protocol. (7 marks)

(8 marks) (ii) Sliding window protocol.

Or

(b) Discuss on :

(i) Link Management.

2 D 23467 (7 marks) (ii) · Selective repeat. (8 marks) V. (a) Explain the three types of frames in HDLC protocol. Explain bit stuffing in HDLC. Or (b) (i) Explain the link access procedures. (8 marks) (ii) Discuss on logical link control in detail. (7 marks) $[4 \times 15 = 60 \text{ marks}]$