

EIGHTH SEMESTER B.TECH (ENGINEERING) DEGREE EXAMINATION, MAY 2012
EC 04 803 - COMMUNICATION SWITCHING SYSTEMS

Time : Three Hours

Maximum : 100 Marks

PART - A*Answer all questions*

- I (a) Derive an expression for blocking probability of a three stage switch.
 (b) Differentiate between the loss system and delay system.
 (c) Briefly explain about the three stage combination switching.
 (d) State the principle of Time division switching.
 (e) What are self routing switches ? Explain.
 (f) Briefly explain about the PCM signalling.
 (g) Explain the principle of TSIC Time slot Interchange.
 (h) How are switching systems classified? In what way is stored program control superior to hard wired control?

PART - B

(8 × 5 = 40)

- II (a) (i) Distinguish between inchshnel and common channel Signalling.
 (ii) Explain basic scheme of common channel signalling.

Or

- (b) Write short notes on:
 (i) Self routing switches.
 (ii) Strict sense non-block switches.

- III (a) Discuss in detail about :
 (i) Lost calls returned system.
 (ii) Lost calls held system.

Or

- (b) Explain the following:-
 (i) Grade of service.
 (ii) Blocking Probability.
 (iii) Traffic Intensity.

- IV (a) (i) Compare in Channel signalling and common channel signalling.
 (ii) Discuss about ATM routers.

Or

- (b) Write notes on :
 (i) PGM signalling.
 (ii) Common channel signalling principle.

- V (a) Explain in detail about time multiplexed space- switching.

Or

- (b) (i) Differentiate between single stage and multistage networks.
 (ii) Mention the various types of switching network configurations.

(4 × 15 = 60)