Name COLLEGE

FOURTH SEMESTER B.TECH. (ENGINEERING) DEGREE EXAM DECEMBER 2010

EC/AI 2K 405 MICROPROCESSOR AND MICROCONTRODLERS

Time: Three Hours

Maximum: 100 Marks

- I. (a) Explain the INTA cycle and memory read cycle of 8086.
 - (b) Write a program to transfer a block of 10 data from one location to another.
 - (c) What are the methods of address decoding? Give examples.
 - (d) Explain the mode 2 configuration of 8255.
 - (e) Explain about memory paging.
 - (f) Bring about the features of pentium and compare it with 8086 and 80386 processors.
 - (g) Explain RAM and ROM memory space of 80196.
 - (h) Explain about PSW, and serial port registers of 80196.

 $(8 \times 5 = 40 \text{ marks})$

II. (a) Discuss in detail the addressing modes of 8086.

Or

- (b) Describe the block diagram of 8086.
- III. (a) Discuss in detail the working of 8279.

Or

- (b) Describe the different modes of operation of 8257.
- IV. (a) Describe the working of 80386 with the help of a block diagram.

Or

- (b) Explain in detail about architecture of pentium processor.
- V. (a) Discuss in detail the I/O interface of 80196.

Or

(b) Explain the working of 80196 with the help of a figure.

 $(4 \times 15 = 60 \text{ marks})$